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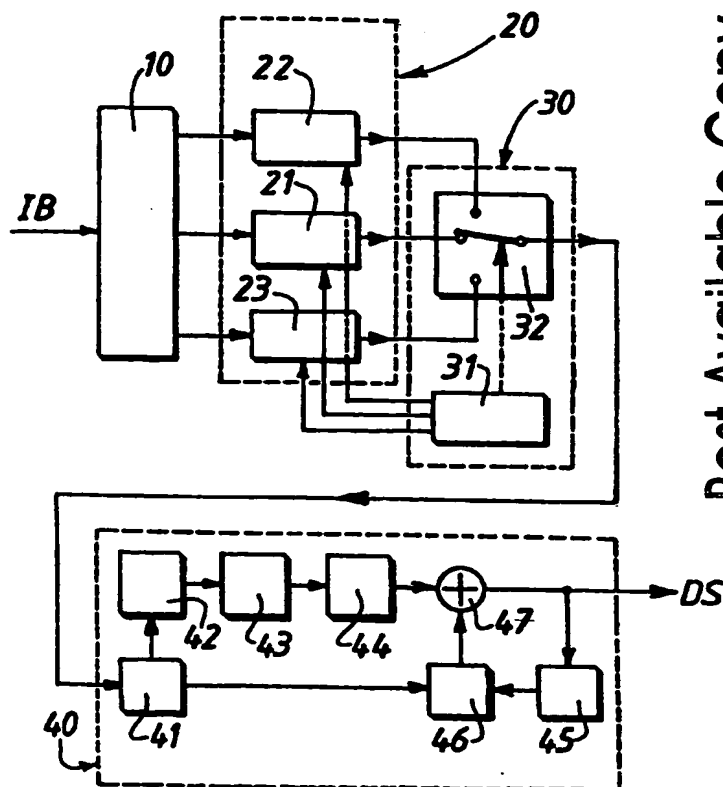
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(54) Title: METHOD AND DEVICE FOR DECODING DIGITAL VIDEO BITSTREAMS AND RECEPTION EQUIPMENT INCLUDING SUCH A DEVICE

(57) Abstract

Decoding method and device with which it is proposed, in order to reduce the switching time from one among n input digital video bitstreams corresponding to n programs to another one, to use under the supervision of control means (30) including a control unit (31) and a switch (32), while a selected digital video bitstream is presented for decoding and display, (n-1) buffers (22, 23) for the (n-1) extra bitstreams, these extra buffers being dimensioned so that they always comprise at least an intra frame, and preferably the most recent one, of each incoming extra bitstream. In a simpler embodiment where the next program switched to is one up or one down, only one or two extra buffers are necessary, subject to the knowledge of an information on how the incoming programs may be accessed, i.e. according to some defined order. Application: reception equipments for decoding of digital television programs in accordance with the MPEG standard.



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"Method and device for decoding digital video bitstreams and reception equipment including such a device"

The present invention relates to a method of decoding a bitstream including a plurality of multiplexed digital video bitstreams corresponding to previously coded signals, said method comprising in cascade the steps of:

- 5 - receiving n digital video bitstreams in the form of a single multiplexed bitstream originating from previous transmitting and/or storage means;
- selecting one single digital video bitstream for display; and
- decoding the selected digital video bitstream.

The invention also relates both to a device for decoding a bitstream including a plurality of multiplexed digital video bitstreams corresponding to previously coded signals, said device comprising:

- 10 - means for receiving n digital video bitstreams in the form of a single multiplexed bitstream originating from previous transmitting and/or storage means and demultiplexing said multiplexed bitstream;
- 15 - storing means;
- means for selecting one single digital video bitstream for display; and
- means for decoding the selected digital video bitstream,

and to a reception equipment comprising such a decoding device.

This invention finds an application in the field of television (standard, high definition, or interactive television for instance), and particularly in the field of video terminals or receivers based on the MPEG standard.

The european patent application n° 0566092 describes an apparatus for decoding compression-coded moving picture signals, comprising, in one of the proposed embodiments, a single decoder provided for decoding an input bitstream composed of multiplexed coded signals corresponding to independant original sequences of images. This apparatus associates to said decoder control means allowing to reconstruct and display from the output signals of the decoder the pictures corresponding to one of the original video sequences. However, the method carried out in such an apparatus does not take into account the problems which appear in case of switching from a sequence to another one and, more

particularly, the troublesome factors which considerably increase the waiting time for pictures from the new video sequence.

5 It is therefore a first object of the invention to propose a method of decoding a bitstream including a plurality of multiplexed digital video bitstreams which allows to switch fast from one of the coded bitstreams to another one.

To this end, the invention relates to a method as defined in the preamble of the description, in which

10 said coded digital signals correspond at least to frames, said intra, which are coded without any reference to any other previous frames, the selected digital video bitstream being presented for decoding and display and coded signals corresponding to an intra frame of at least one of the (n-1) other bitstreams being stored for immediate access by the viewer.

In a particular application of the method,

15 said coded digital signals correspond to frames, said intra, which are coded without any reference to any other frame or to frames said predicted or interpolated according to the fact that, in agreement with the MPEG standard, they are respectively predicted by motion-compensated prediction from an earlier intra or predicted frame or bidirectionally interpolated from an earlier and a later frame, the selected digital video bitstream being
20 presented for decoding and display, and at least the coded signals corresponding to an intra frame of at least one of the (n-1) other bitstreams being stored for immediate access by the viewer.

By implementing this decoding method, especially in the case of coded signals being in agreement with the MPEG standard, switching is thus considerably faster
25 than when a conventional decoder is used. Preferably said stored coded signals are the most recently received intra frame, rather than storing the whole bitstream representing a given period of time. A picture is then displayed nearly instantaneously upon switching to an other video sequence.

In more sophisticated implementations, said stored coded signals belong
30 to the two bitstreams corresponding to the most immediate access by the viewer, or, more generally, to the (n-1) other bitstreams.

It is another object of the invention to propose a device for decoding multiplexed digital video bitstreams which allows to implement said decoding method.

To this end, the invention relates to a device as defined in the preamble

of the description and in which

said coded digital signals correspond at least to frames, said intra, which are coded without any reference to any other previous frame, and said storing means comprise, while the selected digital video bitstream is presented for decoding and display, a buffer for storing
5 coded signals that correspond to an intra frame of at least one of the (n-1) other bitstreams, for immediate access by the viewer.

Notwithstanding the increase of the size of the physical memory, the access delay in case of switching on from one input bitstream to another is considerably reduced, which is considered as very advantageous by the consumers. Only a residual
10 waiting time is to be observed before the concerned sequence begins to exhibit motion.

Obviously the invention also relates, in a particular embodiment, to a decoding device in which said coded digital signals correspond to frames, said intra, which are coded without any reference to any other frame or to frames said predicted or interpolated according to the fact that, in agreement with the MPEG standard, they are
15 respectively predicted by motion-compensated prediction from an earlier intra or predicted frame or bidirectionally interpolated from an earlier and a later frame, and said storing means comprise, while the selected digital video bitstream is presented for decoding and display, a buffer for storing at least the coded signals corresponding to an intra frame of at least one of the (n-1) other bitstreams, for immediate
20 access by the viewer.

In other embodiments, said storing means may comprise two parallel buffers for storing at least the coded signals corresponding to an intra frame of the two bitstreams corresponding to the most immediate access by the viewer, or more generally (n-1) parallel buffers for storing at least the coded signals corresponding to an intra frame of
25 each of the (n-1) other bitstreams.

Preferably said buffer(s) is (are) provided for storing within each of said bitstream(s) the most recently received intra frame. Moreover, the buffers may together constitute a single physical memory.

It is still another object of the invention to propose a reception equipment
30 including such a decoding device.

To this end, the invention relates to a reception equipment comprising a decoding device such as indicated above and provided in the form of a sub-assembly which comprises the following circuits:

- a demultiplexer, provided for separating the input multiplexed bitstream

into n respective demultiplexed digital video bitstreams corresponding to original sequences of images;

- parallel buffers;
- a selector provided for enabling said viewer to select for decoding and display one of said bitstreams stored in one buffer, only a limited part of the other non-selected demultiplexed bitstreams being stored within the $(n-1)$ other buffers, and said limited part being at least the coded signals corresponding to a previous intra frame of each bitstream;

- a decoder of said selected bitstream;
 - display means, in the form of a screen of a television set.
- Preferably, said sub-assembly and said television set constitute one single apparatus.

These and other aspects of the invention will be apparent from and elucidated with reference to the embodiments described hereinafter and considered in connection with the accompanying drawings, wherein:

- Fig.1 is a schematical illustration of a conventional video coding and decoding scheme;
- Fig.2 shows an embodiment of a device carrying out the decoding method according to the present invention;
- Fig.3 is an example of a software program convenient for an implementation of the invention;
- Fig.4 shows a simplified representation of a video reception sub-assembly performing the steps of the method according to the invention.

A typical video compression scheme based on variable length coding and motion compensation, for instance a compression scheme according to the international video standard MPEG, processes digital signals corresponding to images according to the main following steps: orthogonal transform of these signals (such as discrete cosine transform), quantization, variable length coding, memorization, and (in a backward connection) picture prediction by motion estimation and compensation. Reciprocally, a corresponding decoding scheme, and particularly an MPEG decoder, implements the following steps: memorization

of the input coded bitstream, variable length decoding, inverse quantization, inverse orthogonal transform, and reconstruction of pictures by motion-compensated prediction.

Such a decoding scheme concerns the situation where

only one digital coded bitstream has been transmitted and/or stored and is now received for decoding. When the incoming input signals correspond to several sequences or programs embedded in a single large bitstream with possible switching from one sequence or program to another one, switching on to another bitstream (another television program, another interactive video sequence, and so on, according to the activity of the user) will in fact take some time. According for instance to the MPEG standard, described in many documents and for example in the article "MPEG: A Video Compression Standard for Multimedia Applications", D. Le Gall, Communications of the ACM, April 1991, vol.34, n°4, pp.46-58, it is indeed known that the sequence of transmitted and/or stored signals includes periodically spaced intra pictures, i.e. pictures coded without using information from any other picture and which can therefore be considered as reference pictures (providing for example access points for random access). At the time of switching, an access delay corresponding to the waiting time for an intrapicture to occur cannot be avoided.

Moreover, in such switching situations, the waiting time for pictures from the new video bitstream to be available also depends on other factors, such as the time to wait until the input buffer is loaded to the correct filling level before the decoding process can start, the time for reversing the picture order (the coded signals also include predicted pictures obtained by bidirectional interpolation from a previous and a following picture, and therefore decoded only after said corresponding following picture is available at the reception side), and the time to decode a picture.

Referring to Fig.1, there is very schematically illustrated a video coding scheme provided for implementing n parallel coding steps $2a$ to $2n$ followed by a multiplexing step 3 (this number n may be for example equal to 3). After transmission (and/or storage), a corresponding decoding scheme is provided for implementing a demultiplexing step 100 of the received coded bitstream, followed by n storing steps $200a$ to $200n$ and n decoding steps $400a$ to $400n$. According to this decoding method, the demultiplexing step is provided for separating within the single large input coded bitstream the plurality of multiplexed bitstreams.

The proposed decoding method comprises in cascade the steps of receiving n digital video bitstreams in the form of a single multiplexed bitstream originating from previous transmitting and/or storage means, selecting one single digital video bitstream,

and decoding the selected digital video bitstream. Moreover, the coded digital signals correspond to frames, said intra, which are coded without any reference to any other previous frames, and, while the selected digital video bitstream is presented for decoding and display, the coded signals corresponding to an intra frame of at least one of the (n-1) other
5 bitstreams are stored for allowing as soon as requested an immediate access by the viewer.

Thanks to that organization in parallel operations using one or a plurality of buffers, it is possible to reduce the switching time: upon switching from one program or sequence to another, there is indeed no longer need to wait until an intra picture arrives and the corresponding buffer has the desired occupancy.

10 This principle of the invention can be implemented for instance according to the embodiment of Fig.2: the decoding device shown in this figure comprises a demultiplexer 10 which receives an input bitstream IB including a plurality (generally n, only three in the represented embodiment) of individual video bitstreams and the outputs of which (three in the case of Fig.2) are sent to storing means 20 comprising in this embodiment the
15 same plurality of buffers 21 to 23 (three in this example).

A viewer switching action may occur in a control unit 31 provided for selecting the buffer corresponding to the selected bitstream which will be decoded, for instance the buffer 21, and at the same time for controlling the transfer of the two other bitstreams (which at that moment have not to be decoded, since none of them has been
20 selected by a viewer action) towards the two other buffers 22 and 23. At the outputs of parallel buffers 21 to 23, a switch 32 controlled by the control unit 31 with which it constitutes control means 30 selects the appropriate buffer output signal and sends it towards a decoder 40. This decoder comprises for example a demultiplexer 41 (provided for separating the signals to be decoded and associated motion vectors), a variable length
25 decoder 42, a dequantizer 43, an inverse orthogonal transform circuit 44, and a prediction circuit comprising a memory 45, a motion compensation circuit 46 (receiving from the demultiplexer 41 the motion vectors), and an adder 47 which adds the outputs of the motion compensation circuit 46 and the inverse orthogonal transform circuit 44 and yields the output decoded signals DS to be displayed (such a decoder is conventional and will not be described
30 in a more detailed manner).

The buffers 22 and 23 receive the respective bitstreams and store a limited part of each one, including at any moment at least an intra frame. Therefore, upon switching from a bitstream to another, an intra frame of this other selected bitstream is already ready to be decoded, and the viewer has no longer to wait for another displayed

signal (in case of a conventional MPEG system and a frame frequency of 25 Hz, only each 12th picture corresponds to an intraframe, which corresponds to a waiting time of $12/25 = 0,48$ seconde).

The invention is obviously not limited to the embodiment described hereinbefore, from which variations or improvements may be conceived without departing from the scope of the invention. It should for instance be noted that, especially with respect to the control means, the proposed implementation of the invention may be partly in the form of the execution of a series of programmed instructions the performance of which is for example ensured by a microprocessor:

An example of a software program for such an implementation is illustrated in Fig.3, that shows a flowchart detailing the reception process of the input coded bitstream (in relation with the embodiment of Fig.2 for the explanations). The coded information is first received at 301 (this reference corresponds to the input of the demultiplexer 10), in the form of the coded bitstream corresponding, before transmission and/or storage, to the original digital signals from the n parallel channels (see the coding scheme of Fig.1). The received coded bitstream is demultiplexed (sub-step 302). The program then determines (sub-step 303) which particular bitstream has to be selected, upon the indication from a television viewer 310, and this coded bitstream is stored if necessary (sub-step 304a), and then decoded (sub-step 305) and displayed (sub-step 306), the output decoded signals being referenced DS in Figs 2 and 3. In the same time a limited part of each of the bitstreams which have not been selected for decoding, including at least an intra picture, is maintained (sub-steps 304b, ..., 304n) in a place of the buffers in which these limited parts are ready to be transferred out of these buffers (if necessary, i.e. upon switching from the previously selected bitstream to another one) for an immediate access by the viewer. Therefore, when such a situation occurs, the stored intra frame of this newly selected bitstream can be immediately read (sub-step 307), decoded (sub-step 305) and displayed (sub-step 306), as already explained, without any waiting time. The cycle of demultiplexing, storing and decoding the bitstream is then repeated.

Moreover, the invention also relates to a reception equipment for implementing the above-indicated operations. There is illustrated in Fig.4 a simplified representation of a video reception sub-assembly 400 that performs the steps of the method according to the invention. Such a reception sub-assembly is connected (at viewer's home or place) between an antenna or cable outlet and a conventional television set, or preferably incorporated inside the television set.

As already explained, the incoming bitstream corresponds (at the side of the transmitter) to coded multiplexed signals associated to n parallel channels and corresponding to original images. The video reception apparatus 400 includes a demultiplexer 401 that separates the input coded bitstream in respective digital video bitstreams
5 corresponding to each of the respective original sequences of images. A selector 402 enabling the viewer to select any desired channel allows to select the corresponding bitstream which has been stored in one of the buffers of storage means 403. From the other non-selected demultiplexed bitstreams, only a limited part, for example a previous or the preceding intra picture, is stored. At the output of the storage means 403, a decoder 404 performs a data
10 decompression corresponding to the compression carried out at the side of the transmitter and carries out the reconstruction of pictures which are then displayed on the screen 405 of the concerned television set.

Another improvement, concerning an alternative fast switching embodiment may also be as follows. Rather than storing a predetermined period of time of
15 each bitstream in at least one or in the (n-1) extra buffers not concerned at a given moment by the decoding step, only the most relevant part thereof may be stored. For instance, only a recently received, or the most recently received, intra frame of each bitstream is stored, which leads to the fact that a picture is now displayed nearly instantaneously, only a short waiting time being observed before the program starts to exhibit motion.

20 It must also be indicated that the various buffers are either distinct or may constitute a single physical memory. In all the cases, the required buffer size BS for the extra buffers depends on the bitrate and the frequency of the intra frames within the bitstreams:

$$BS = ((N/f_f) \cdot R) + BD$$

25 where: N = number of pictures within a group of pictures (i.e every Nth picture is an intra one);

f_f = frame frequency;

R = bitrate;

BD = size of the decoder buffer.

30 Therefore, if N = 12 and the bitrate value is 5 Megabits/sec., extra buffers of $((12/25) \times 5) + 1,8 = 4,2$ Mbits/s are desired (the value 1,8 Mbit/s being here the input buffer size as specified in MPEG main profile: for higher bitrates, a larger buffer is obviously required, with a maximum buffer size of 9 Mbits for a bitrate of 15 Mbits/s which is the maximum value in MPEG main profile at main level).

- It can still be noticed that simpler implementations of the decoding method and device are possible. For instance, in the case of switching to another incoming TV program, it is most likely that the next program switched to is either one up or one down, which allows to provide for only two parallel operations in place of n , the first one
- 5 for decoding the signals of the selected bitstream and displaying them and the other for storing, within the other bitstream which corresponds to the next possible selection by the viewer (it is indeed now required, in the case of this simpler implementation, that an information is available on how the incoming programs may be accessed, i.e. according to which defined order), at least the coded signals corresponding to an intra frame.
 - 10 Alternatively, said second storing operation may concern two bitstreams, corresponding to the two nearest possible selections according to the viewer decision.

CLAIMS:

1. A method of decoding a bitstream including a plurality of multiplexed digital video bitstreams corresponding to previously coded signals, said method comprising in cascade the steps of:

- receiving n digital video bitstreams in the form of a single multiplexed
- 5 bitstream originating from previous transmitting and/or storage means;
- selecting one single digital video bitstream for display; and
- decoding the selected digital video bitstream;

wherein said coded digital signals correspond at least to frames, said intra, which are coded without any reference to any other previous frames, the selected digital video bitstream being

10 presented for decoding and display, and coded signals corresponding to an intra frame of at least one of the (n-1) other bitstreams being stored for immediate access by the viewer.

2. A decoding method according to Claim 1, wherein said coded digital signals correspond to frames, said intra, which are coded without any reference to any other frame or to frames said predicted or interpolated according to the fact that, in agreement

15 with the MPEG standard, they are respectively predicted by motion-compensated prediction from an earlier intra or predicted frame or bidirectionally interpolated from an earlier and a later frame, the selected digital video bitstream being presented for decoding and display, and at least the coded signals corresponding to an intra frame of at least one of the (n-1) other bitstreams being stored for immediate access by the viewer.

20 3. A decoding method according to Claim 2, in which said stored coded signals are the most recently received intra frame.

4. A decoding method according to anyone of Claims 1 to 3, in which said stored coded signals belong to the two bitstreams corresponding to the most immediate access by the viewer.

25 5. A decoding method according to anyone of Claims 1 to 3, in which said stored coded signals belong to the (n-1) other bitstreams.

6. A device for decoding a bitstream including a plurality of multiplexed digital video bitstreams corresponding to previously coded signals, said device comprising:

- means for receiving n digital video bitstreams in the form of a single

multiplexed bitstream originating from previous transmitting and/or storage means and demultiplexing said multiplexed bitstream;

- storing means;
- means for selecting one single digital video bitstream for display; and
- means for decoding the selected digital video bitstream;

wherein said coded digital signals correspond at least to frames, said intra, which are coded without any reference to any other previous frame, and said storing means comprise, while the selected digital video bitstream is presented for decoding and display, a buffer for storing coded signals that correspond to an intra frame of at least one of the (n-1) other bitstreams, for immediate access by the viewer.

7. A decoding device according to Claim 6, wherein said coded digital signals correspond to frames, said intra,

which are coded without any reference to any other frame or to frames said predicted or interpolated according to the fact that, in agreement with the MPEG standard, they are

respectively predicted by motion-compensated prediction from an earlier intra or predicted frame or bidirectionally interpolated from an earlier and a later frame, and said storing means comprise, while the selected digital video bitstream is presented for decoding and display, a buffer for storing at least the coded signals corresponding to an intra frame of at least one of the (n-1) other bitstreams, for immediate access by the viewer.

8. A decoding device according to Claim 7, in which said storing means comprise two parallel buffers for storing at least the coded signals corresponding to an intra frame of the two bitstreams corresponding to the most immediate access by the viewer.

9. A decoding device according to Claim 7, in which said storing means comprise (n-1) parallel buffers for storing at least the coded signals corresponding to an intra frame of each of the (n-1) other bitstreams.

10. A decoding device according to anyone of Claims 7 to 9, in which said buffer(s) is (are) provided for storing within each of said bitstream(s) the most recently received intra frame.

11. A decoding device to anyone of Claims 7 to 10, in which said parallel buffers together constitute a single physical memory.

12. A reception equipment comprising a decoding device according to anyone of claims 6 to 11, in the form of a sub-assembly comprising the following circuits:

- a demultiplexer, provided for separating the input multiplexed bitstream into n respective demultiplexed digital video bitstreams corresponding to original sequences of images;

- parallel buffers;

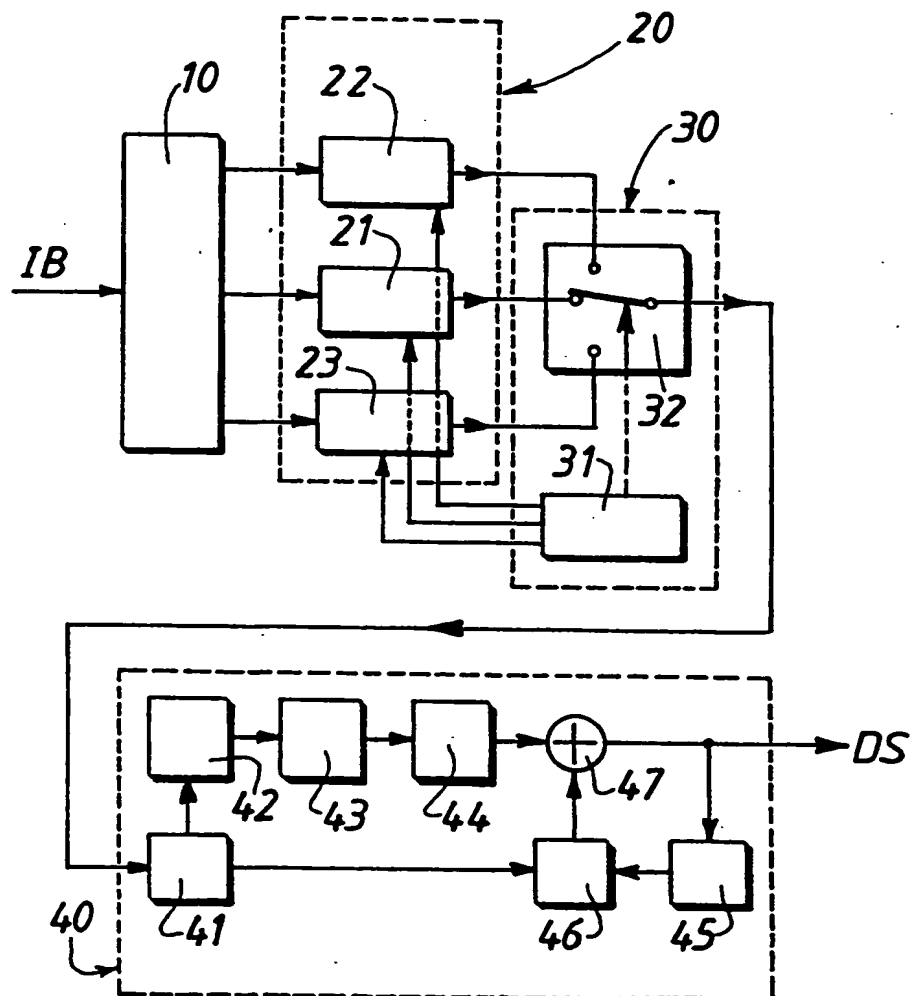
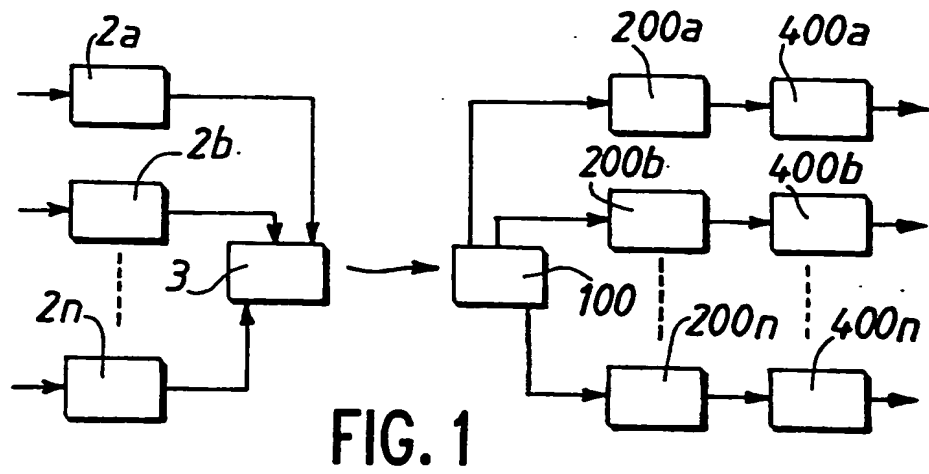
5 - a selector provided for enabling said viewer to select for decoding and display one of said bitstreams stored in one buffer, only a limited part of the other non-selected demultiplexed bitstreams being stored within the $(n-1)$ other buffers, and said limited part being at least the coded signals corresponding to a previous intra frame of each bitstream;

10 - a decoder of said selected bitstream;

- display means, in the form of a screen of a television set.

13. A reception equipment according to Claim 12, wherein said sub-assembly and said television set constitute one single apparatus.

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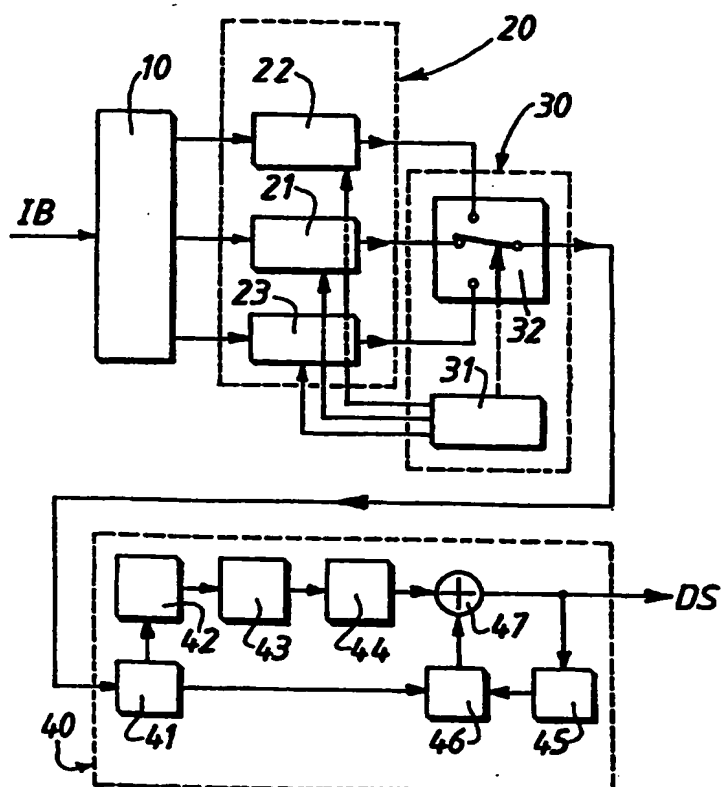
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(57) Abstract

Decoding method and device with which it is proposed, in order to reduce the switching time from one among n input digital video bitstreams corresponding to n programs to another one, to use under the supervision of control means (30) including a control unit (31) and a switch (32), while a selected digital video bitstream is presented for decoding and display, $(n-1)$ buffers (22, 23) for the $(n-1)$ extra bitstreams, these extra buffers being dimensioned so that they always comprise at least an intra frame, and preferably the most recent one, of each incoming extra bitstream. In a simpler embodiment where the next program switched to is one up or one down, only one or two extra buffers are necessary, subject to the knowledge of an information on how the incoming programs may be accessed, i.e. according to some defined order. Application: reception equipments for decoding of digital television programs in accordance with the MPEG standard.



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INTERNATIONAL SEARCH REPORT

International application No.

PCT/IB 96/00662

A. CLASSIFICATION OF SUBJECT MATTER

IPC6: H04N 5/262, H04N 7/26, H04N 7/50, H04N 7/58

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC6: H04N

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 0566092 A2 (SONY CORPORATION), 20 October 1993 (20.10.93), column 6, line 29 - column 7, line 59 --	1-13
A	Patent Abstracts of Japan, Vol 18, No 155, E-1524, abstract of JP,A,5-328344 (MITSUBISHI ELECTRIC CORP), 10 December 1993 (10.12.93) --	1-13
A	Communications of the ACM. p. 46-58, Volume 34, No 4, April 1991, (NEW YORK, US), D. Le Gall, "MPEG A Video Compression Standard for Multimedia Applications", see page 51, centre column, line 46 - page 52, right column, line 52 --	1,2,6,7,12, 13

☒ Further documents are listed in the continuation of Box C.☒ See patent family annex.

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Z document member of the same patent family

Date of the actual completion of the international search

6 February 1997

Date of mailing of the international search report

10 -02- 1997

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C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5361098 A (KEITH LUCAS), 1 November 1994 (01.11.94), column 4, line 37 - column 5, line 12, figure 2 ---	1,2,6,7,12, 13
A	US 5029014 A (JAMES E. LINDSTROM), 2 July 1991 (02.07.91), column 2, line 58 - column 3, line 8 ---	1,6,12
P,A	WO 9528795 A2 (PHILIPS ELECTRONICS N.V. ET AL), 26 October 1995 (26.10.95), column 5, line 28 - column 8, line 11, figures 5,6,7 -----	1,2,6,7,12, 13

INTERNATIONAL SEARCH REPORT
Information on patent family members

28/10/96

International application No.
PCT/IB 96/00662

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP-A2- 0566092	20/10/93	JP-A- 5292490 US-A- 5343250	05/11/93 30/08/94
US-A- 5361098	01/11/94	AU-A- 5730994 CA-A- 2150467 EP-A- 0671102 JP-T- 8503828 WO-A- 9413101	22/06/94 09/06/94 13/09/95 23/04/96 09/06/94
US-A- 5029014	02/07/91	NONE	
WO-A2- 9528795	26/10/95	AU-A- 2082195 AU-A- 2082295 WO-A- 9528794	10/11/95 10/11/95 26/10/95

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